

*Fig. 1*

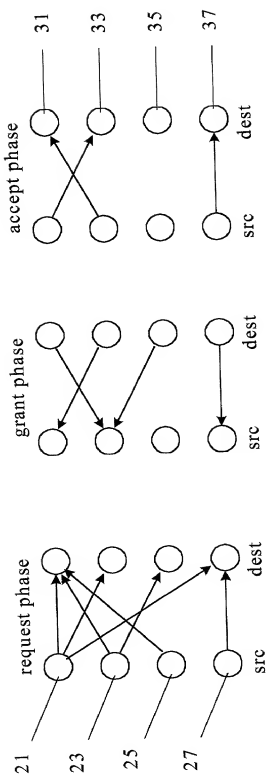


Fig. 2

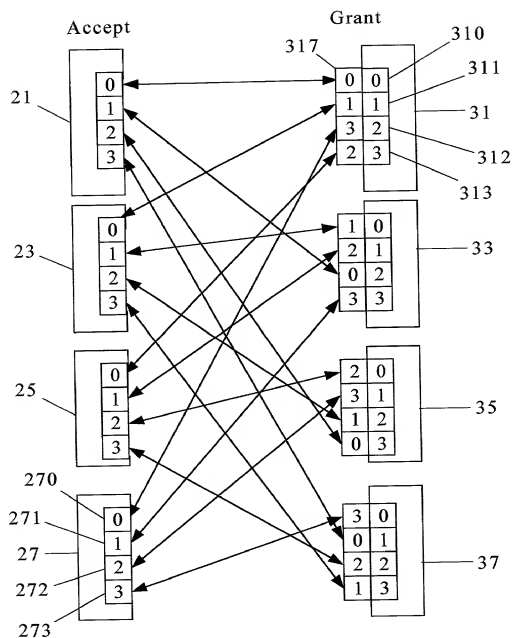


Fig. 3(a)

		Egress Port Number			
		0	1	2	3
Original Pointer Value	0	0	1	2	3
	1	1	2	3	0
	2	3	0	1	2
	3	2	3	0	1
		Mapped Pointer Value			

Fig. 3(b)


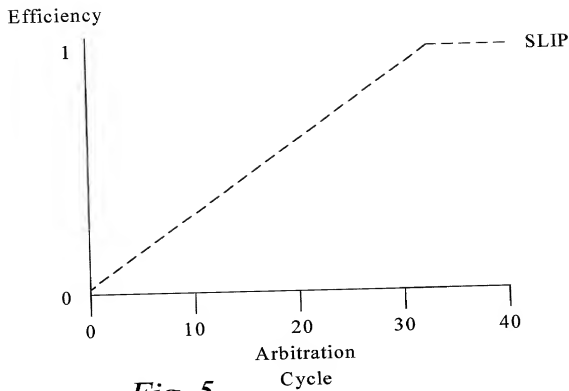
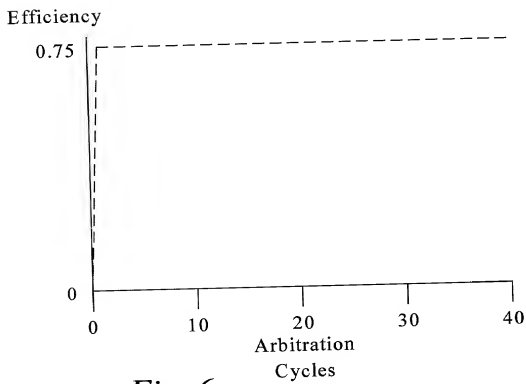
M	P 							
	0	1	2	3	4	5	6	7
0	0	1	2	3	4	5	6	7
1	1	2	3	4	5	6	7	0
2	3	4	5	6	7	0	1	2
3	6	7	0	1	2	3	4	5
4	2	3	4	5	6	7	0	1
5	7	0	1	2	3	4	5	6
6	5	6	7	0	1	2	3	4
7	4	5	6	7	0	1	2	3

Fig. 4

*Fig. 5**Fig. 6*